**R.V COLLEGE OF ENGINEERING**



**Department of Computer Science and Engineering**

**Logic Design Course End Survey Questions**

1. **How would you rate the prescribed syllabus of LD? CO2**

**Excellent**

**Very Good**

**Good**

**Satisfactory**

1. **To what extent the basics are covered in each unit? CO1**

**Very Good**

**Good**

**To some extent**

**None**

1. **Did the subject motivate you to design any real time systems? CO3**

**Yes**

**No**

1. **How much apt is the subject for computer science students? CO2**

**Very closely related**

**Highly related**

**Related to some extent**

**Not related**

1. **Are the topics in LD subject more application based or theory based? CO3**

**Purely Theoretical**

**Purely Application Based**

**Both**

**None**

1. **Does the introduction to Simulation software design is helpful? CO2**

**Yes**

**No**

1. **Do you want more complex circuits or designs to be included as part of lab? CO2**

**Yes**

**No**

1. **Does the subject teach you all that is required to make a real time product out of it? CO4**

**Yes**

**No**

1. **Do you think the subject will help you in future? CO1**

**Yes**

**No**

1. **How do you rate the difficulty level of the subject as well as the lab? CO2**

**Very Difficult**

**Difficult**

**Easy**

**Very Easy**